DERWENT-ACC-NO:

2003-172416

DERWENT-WEEK:

200317

COPYRIGHT 1999 DERWENT INFORMATION LTD

TITLE:

Apparatus and method for baking

wafer

INVENTOR: SHIN, G H

PATENT-ASSIGNEE: SAMSUNG ELECTRONICS CO LTD[SMSU]

PRIORITY-DATA: 2001KR-0003432 (January 20, 2001)

PATENT-FAMILY:

PUB-NO

PUB-DATE

LANGUAGE PAGES

MAIN-IPC

KR 2002062437 A

July 26, 2002

N/A

001

H01L 021/027

APPLICATION-DATA:

PUB-NO

APPL-DESCRIPTOR

APPL-NO

APPL-DATE

KR2002062437A

N/A

2001KR-0003432

January 20, 2001

INT-CL (IPC): H01L021/027

ABSTRACTED-PUB-NO: KR2002062437A

BASIC-ABSTRACT:

NOVELTY - An apparatus for baking a wafer is provided to prevent a process

defect caused by a wafer inaccurately placed on a heating plate by making a

detecting unit using a sensor and a lift pin detect the wafer placed on the

heating plate, and to prevent recurrence of a defect by readjusting a loading position of a robot.

DETAILED DESCRIPTION - A wafer to bake is placed on the heating plate (110).

The lift pin loads/unloads the wafer to/from the heating plate, installed in the heating plate. A guide pin guides the wafer loaded to the heating plate, installed at the edge of the heating plate. A wafer detecting unit detects the wafer inaccurately loaded to the heating plate, installed in the guide pin.

CHOSEN-DRAWING: Dwg.1/10

TITLE-TERMS: APPARATUS METHOD BAKE WAFER

DERWENT-CLASS: U11

EPI-CODES: U11-C03A;

